

FIG. 1 -PRIOR ART-

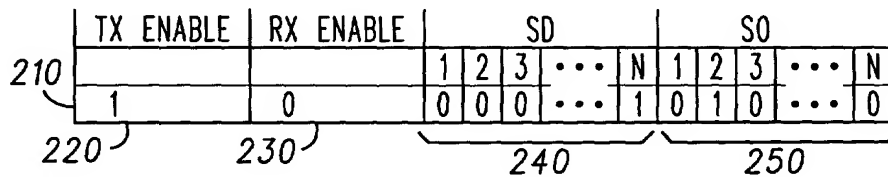


FIG. 2

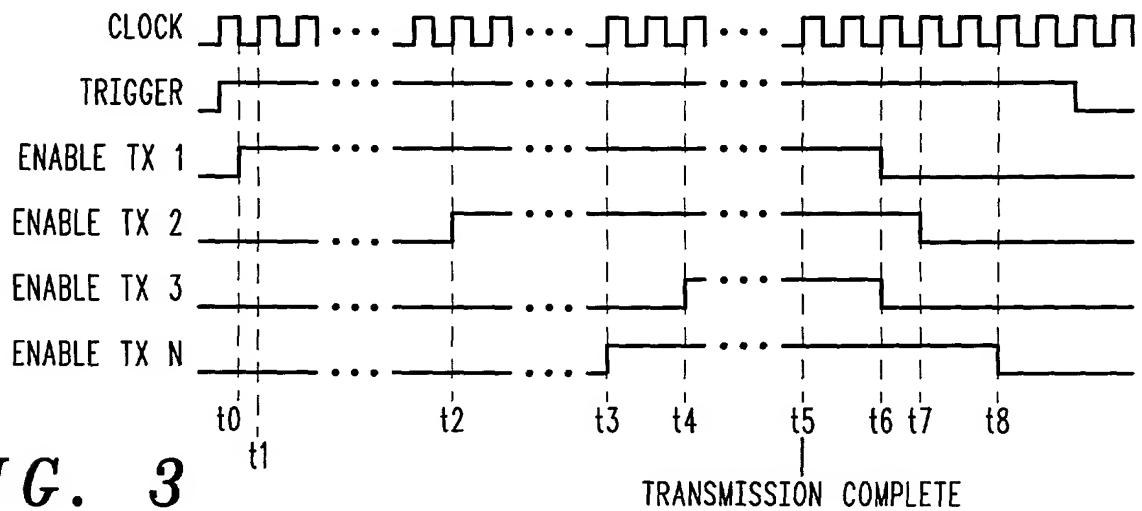


FIG. 3

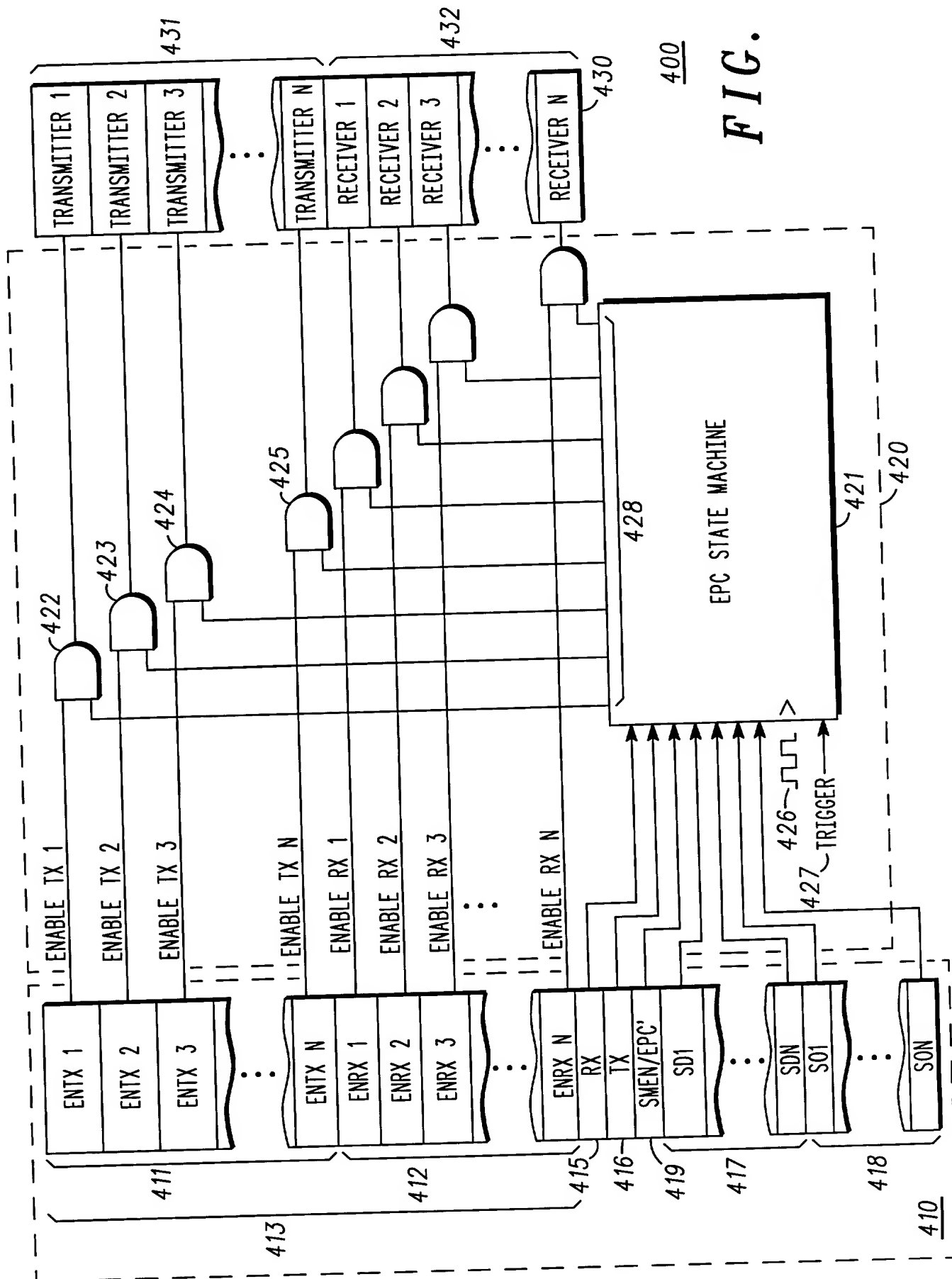


FIG. 4

	TX ENABLE	RX ENABLE	SMEN/EPC'	SD				S0				ENABLE TX				ENABLE RX			
				1	2	3	...	N	1	2	3	...	N	1	2	3	...	N	
510	0	1	1	0	0	0	...	1	1	0	1	...	1	1	1	1	0	...	1
520	X	X	0	X	X	X	...	X	X	X	X	...	X	1	1	1	0	1	0

FIG. 5